

# MSBL-16CS

LVDS Digital Buffer Adapter

## IMAGE



## DEFINITION

- MSBL-16CS는 신호 소스중 TMD5 신호를 LVDS 신호로 변경하여 PDP Module을 구동하는 기기입니다.

## SPECIFICATION

LVDS Output	8~16Bit Single/Dual Support
Maximum Permissible Frequency	* HD : 130MHz * Full HD : 148.5MHz
Input Connector	DVI-I
Output Connector	MDR68FEL
Power Consumption	DV+5V, Max 2A
Dimension(WXDXH)	128 X 160 X 30mm, Weight : 0.4Kg
Note	* Internal & External Power convert option. * Cover noise up to 1.5M LVDS cable. * EDID Copy & Read available. (PC to LVDS buffer) * Select FHD or HD output by replace exclusive LVDS cable.

\* All specifications are subject to change without any notice.

전화. 055-297-8880 / 팩스. 055-256-7388

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